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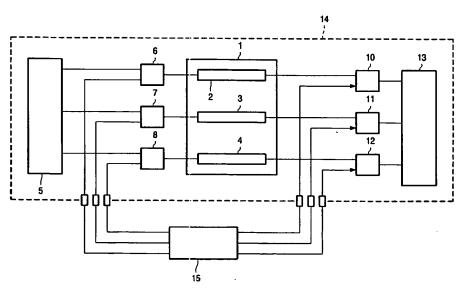
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[Continued on next page]

(54) Title: INTEGRATED CIRCUIT WITH SELF-TESTING CIRCUIT



(57) Abstract: An integrated circuit (14) with an application circuit (1) to be tested and a self-testing circuit (5-13), which is provided for testing the application circuit (1) and generates pseudorandom test patterns, which can be transformed, by means of first logic gates (6, 7, 8) and signals externally fed to said gates, into deterministic test vectors, which are fed to the application circuit (1) for testing purposes, wherein the output signals occurring through the application circuit (1) as a function of the test patterns are evaluated by means of a signature register (13), wherein, by means of second logic gates (10, 11, 12) and signals fed to said gates, those bits of the output signals of the application circuit (1) which, due to the circuit structure of application circuit (1), have undefined states, are blocked during testing.



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INTERNATIONAL SEARCH REPORT

Integral Application No PCT/IB 03/00053

A. CCLASSIFICATION OF SUBJECT MATTER IPC 7 GOTR31/3187 GOTR31/3185 According to international Patent Classification (IPC) or to both national classification and IPC B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC 7 GOTR Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms userd) EPO—Internal , WPI Data , PAJ C. DOCUMENTS CONSIDERED TO BE RELEVANT Category Citation of document, with indication, where appropriate, of the relevant passages Relevant to data No. X, P EP 1 178 322 A (PHILIPS CORF INTELLECTUAL PTY ; KONINKL PHILIPS ELECTRONICS NV (NL)) 6 February 2002 (2002–02–06) the whole documents -/ -/ Private documents are listed in the continuation of box C. X Patent family members are listed in annex. -/ Patent family members are listed in the continuation of box C. X Patent family members are listed in annex. Patent family members are listed in the continuation of box C. X Patent family members are listed in annex. Image: Comparison of the precision of the process of the proce		TO A TROU OF OUR PROPERTY.					
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INTERNATIONAL SEARCH REPORT

Inter I Application No
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INTERNATIONAL SEARCH REPORT

Information on patent family members

Inte val Application No
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